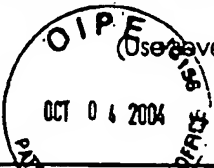


FORM PTO - 1449 (Modified)  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>   (Use several sheets if necessary)  Sheet 1 of 6	<b>Application Number</b>	10/790,415 <del>790,415</del> 791,415
	<b>Filing Date</b>	March 2, 2004
	<b>First Name of Inventor</b>	Santosh P. Gaur et al
	<b>Group Art Unit</b>	2131
	<b>Examiner Name</b>	unknown
	<b>Attorney Docket Number</b>	RPS920020015US1

UNITED STATES PATENT DOCUMENTS						
Examiner Initials	Cite No.	Patent Document Number	Kind Code	Inventor	Date of Publication mm/dd/yyyy	Pages, Columns, Lines Where Relevant Passages Appear
/K.A./	P1	4 817 140		Chandra et al.	03/28/1989	
	P2	5 088 033		Binkley et al.	02/11/1992	
	P3	5 247 577		Bailey et al.	09/21/1993	
	P4	5 430 850		Papadopoulos et al.	07/04/1995	
	P5	5 430 874		Kumazaki et al.	07/04/1995	
	P6	5 432 848		Butter et al.	07/11/1995	
	P7	5 446 906		Kardach et al.	08/29/1995	
	P8	5 619 660		Scheer et al.	04/08/1997	
	P9	5 663 896		Aucsmith	09/02/1997	
	P10	5 699 460		Kopet et al.	12/16/1997	
	P11	5 712 800		Aucsmith	01/27/1998	
	P12	5 719 436		Kuhn	02/17/1998	
	P13	5 724 027		Shipman et al.	03/03/1998	
	P14	5 757 919		Herbert et al.	05/26/1998	
	P15	5 793 101		Kuhn	08/11/1998	
	P16	5 818 939		Davis	10/06/1998	
	P17	5 822 255		Uchida	10/13/1998	
	P18	5 844 986		Davis	12/01/1998	
↓	P19	5 892 899		Aucsmith et al.	04/06/1999	
Examiner Signature /Kaveh Abrishamkar/				Date Considered 10/15/2007		
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<b>FORM PTO - 1449 (Modified)</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)  Sheet 2 of 6	<b>Application Number</b>	10/790,416- 791,415
	<b>Filing Date</b>	March 2, 2004
	<b>First Named Inventor</b>	Santosh P. Gaur et al
	<b>Group Art Unit</b>	2131
	<b>Examiner Name</b>	unknown
	<b>Attorney Docket Number</b>	RPS920020015US1

UNITED STATES PATENT DOCUMENTS						
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/K.A./	P20	5 930 483		Cummings et al.	07/27/1999	
	P21	5 937 063		Davis	08/10/1999	
	P22	5 940 591		Boyle et al.	08/17/1999	
	P23	5 941 987		Davis	08/24/1999	
	P24	5 949 881		Davis	09/07/1999	
	P25	5 968 176		Nessett et al.	10/19/1999	
	P26	5 991 797		Futral et al.	11/23/1999	
	P27	6 006 330		Soni	12/21/1999	
	P28	6 009 527		Traw et al.	12/28/1999	
	P29	6 011 910		Chau et al.	01/04/2000	
	P30	6 014 729		Lannan et al.	01/11/2000	
	P31	6 018 767		Fijolek et al.	01/25/2000	
	P32	6 021 201		Bakkle et al.	02/01/2000	
	P33	6 026 085		Chau et al.	02/15/2000	
	P34	6 038 320		Miller	03/14/2000	
	P35	6 047 375		Easter et al.	04/04/2000	
↓	P36	2002/0099855		Bass et al.	07/25/2002	
Examiner Signature /Kaveh Abrishamkar/				Date Considered 10/15/2007		
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<b>FORM PTO - 1449 (Modified)</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)  Sheet 3 of 6	<b>Application Number</b>	10/ <del>798,415</del> 791,415
	<b>Filing Date</b>	March 2, 2004
	<b>First Named Inventor</b>	Santosh Gaur et al
	<b>Group Art Unit</b>	2131
	<b>Examiner Name</b>	unknown
	<b>Attorney Docket Number</b>	RPS920020015US1

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No.	Patent Document Number	Kind Code	Country	Date of Publication mm/dd/yyyy	Pages, Columns, Lines Where Relevant Passages Appear
/K.A./	F1	99/14881		WO	2/25/1999	
/K.A./	F2	0 893 751		EP	01/27/1999	
/K.A./	F3	091 71500		JP	06/30/1997	
<b>Examiner Signature</b> /Kaveh Abrishamkar/				<b>Date Considered</b> 10/15/2007		
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	<b>First Named Inventor</b>	Santosh P. Gaur et al
	<b>Group Art Unit</b>	2131
	<b>Examiner Name</b>	unknown
	<b>Attorney Docket Number</b>	RPS920020015US1

OTHER ART (Including Author (CAPITAL LETTERS), Title, Date, Pertinent Pages, etc.)		
Examiner Initials	Cite No.	
	N1	ALLEN Jr., J.R., et al., IBM PowerNP Network Processor: Hardware, software, and application, IBM Journal of Research & Development, March/May 2003, pp.177-193, Vol. 47, No. 2-3, IBM, USA.
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	N6	EASTER, RJ, et al., S/390 Enterprise Server CMOS cryptographic coprocessor, IBM Journal of Research and Development, Sept.-Nov. 1999, pp. 761-776, Vol. 43, No. 5-6, IBM, USA.
	N7	GAY C., Memory supervision with the M68000 processor. In Realisation with the PMMU component, Elektronik, June 12, 1997, pp. 94-96, 98, Vol. 36, No. 12, West Germany.
	N8	GORDON, DAVIS et al, U.S. Application 09/542,189, Network Processor with Multiple Instruction Threads, IBM Docket RAL920000008US1, filed April 4, 2000.
	N9	HERMANN K., et al., A programmable processing element dedicated as building block for a large area integrated multiprocessor system, 1996 proceedings. Eighth Annual IEEE International Conference on Innovative Systems in Silicon (Cat. No. 96CH35996), 1996, pp. 98-103, IEEE, USA.
Examiner Signature /Kaven Abrishamkar/		Date Considered 10/15/2007
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FORM PTO - 1449 (Modified)  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)  Sheet 5 of 6	<b>Application Number</b>	10/790,415- 791,415
	<b>Filing Date</b>	March 2, 2004
	<b>First Named Inventor</b>	Santosh P. Gaur et al
	<b>Group Art Unit</b>	2131
	<b>Examiner Name</b>	unknown
	<b>Attorney Docket Number</b>	RPS920020018US1

OTHER ART (Including Author (CAPITAL LETTERS), Title, Date, Pertinent Pages, etc.)		
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	N11	KONDO T., et al., Single-board SIMD processors using gate-array LSIs for parallel processing, IEICE Transactions on Electronics, December 1993, pp. 1827-1834, Vol. E76-C, No. 12, Japan.
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	N15	MCCAULEY, D.E., Shared Memory Model for a Dual-Processor File Server, IBM Technical Disclosure Bulletin, Vol. 34, No. 9, February 1992 pp. 336-337
	N16	MELEAR C, Floating point techniques using MC88000, WESCON/90 Conference Record, 1990, pp. 197-204, Los Angeles, CA, USA.
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Examiner Signature		Date Considered
/Kaveh Abrishamkar/		10/15/2007
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	<b>Examiner Name</b>	unknown
	<b>Attorney Docket Number</b>	RPS920020015/S1

OTHER ART (Including Author (CAPITAL LETTERS), Title, Date, Pertinent Pages, etc.)		
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	N22	Task Switching Between Processing Using the Sequencer, IBM TDB, Vol 35, No. 7, December 1992, pp362-363
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